

<b>Notice of References Cited</b>	Application/Control No. 09/964,950	Applicant(s)/Patent Under Reexamination SHAH ET AL.	
	Examiner Lewis A. Bullock, Jr.	Art Unit 2127	Page 1 of 1

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	B	US-6,832,378	12-2004	Beatty et al.	718/104
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	K	US-			
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IBM Technical Disclosure Bulletin. "Synchronization Constructs for Parallel Fortran." February 1, 1987.
	V	IBM Technical Disclosure Bulletin. "Method of Managing Main Storage Areas that are Shared Hierarchically by Threads." January 1994.
	W	Aho, Alfred et al. "Compilers, Principles, Techniques, and Tools. Addison-Wesley series in Computer Science. 1986. pgs. 4-5 and 16-19.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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